

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

HEA-SUK JUNG

Application No.:

Art Group:

Examiner:

Filed:

For: **delay locked loop (dll) in semiconductor device**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL OF FORMAL DRAWINGS

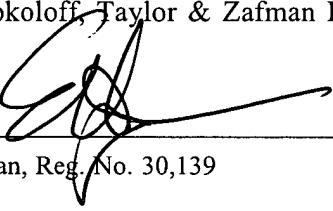
Sir:

Enclosed herewith for filing in the above-identified U.S. Patent Application are the formal drawings, 11 sheets including 11 Figures. Please charge any additional fees or credit any overpayment to Deposit Account No. 02-2666. A duplicate copy of the Fee Transmittal is enclosed for this purpose.

Respectfully submitted,
Blakely, Sokoloff, Taylor & Zafman LLP

Dated:

8/3/03


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